

SF51G-SF58G



Technical Data Data Sheet N2267, Rev. -





Circuit Diagram



Features

- Super fast switching for high efficiency
- Low leakage current
- High forward surge capability
- Solder dip 260 °C, 40 s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC
- Terminals finish: 100% Pure Tin
- This is a Pb Free Device
- All SMC parts are traceable to the wafer lot
- Additional testing can be offered upon request

Mechanical Data

- Case: JEDEC DO-201AD molded plastic body
- Terminals: Plated axial leads, solderable per MIL-STD-750, Method 2026
- Polarity: Color band denotes cathode end
- Mounting Position: Any
- Weight: 1.02 gram

Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

Characteristic	Symbol	SF51G	SF52G	SF53G	SF54G	SF55G	SF56G	SF58G	Units
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	150	200	300	400	600	V
Maximum RMS voltage	V _{RMS}	35	70	105	140	210	280	420	V
Maximum DC blocking voltage	VDC	50	100	150	200	300	400	600	V
Maximum average forward rectified current 0.375 "(9.5mm) lead length at T _A (FIG1)	I _(AV)	5.0			1	Α			
Peak forward surge current 8.3ms single half sine- wave superimposed on rated load (JEDEC Method)	I _{FSM}	150.0			А				
Maximum instantaneous forward voltage at 5.0A	V _F		0.	.95		1.3		1.7	V
Maximum DC reverse current T _A =25 $^{\circ}$ C at rated DC blocking voltage T _A =125 $^{\circ}$ C	I _R	5.0 150.0			μA				
Maximum Reverse Recovery Time (Note 1)	Trr	35			ns				
Typical Junction Capacitance (Note 2)	CJ	100.0 80.0			pF				
Typical Thermal Resistance (Note 3)	R _{0JA}	15.0			°C/W				
Junction Temperature	TJ	-55 to +150			°C				
Storage Temperature Range	T _{STG}	-55 to +150			°C				

Note: 1. Reverse recovery condition IF=0.5A, IR=1.0A. Irr=0.25A

2. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

3. Thermal resistance from junction to ambient at 0.375"(9.5mm) lead length, P.C.B mounted.

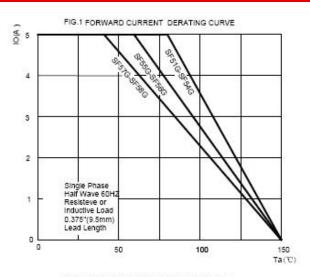
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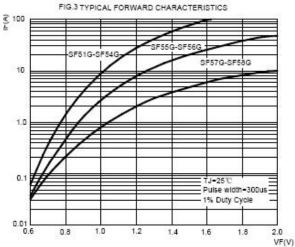


SF51G-SF58G

RoHS 🗭

Ratings and Characteristics Curves





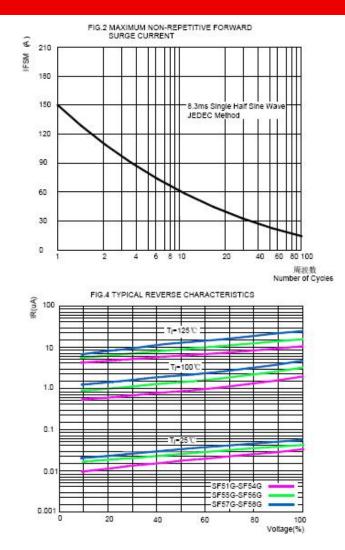
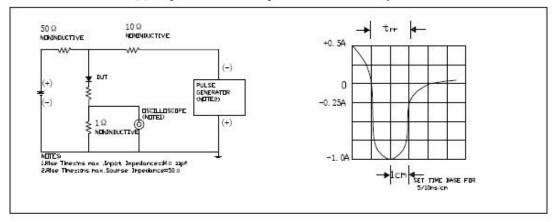


FIG.5 Diagram of circuit and Testing wave form of reverse recovery time



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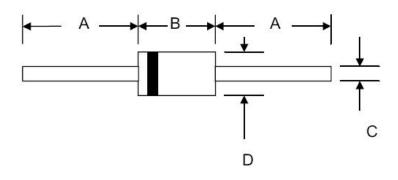


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Mechanical Dimensions DO-201AD



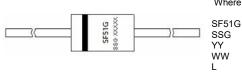
OVMDOL	Millim	neters	Inches			
SYMBOL	Min.	Max.	Min.	Max.		
А	25.4	-	1.000	-		
В	8.50	9.50	0.335	0.374		
С	1.2	1.3	0.048	0.052		
D	5.0	5.6	0.197	0.220		

Ordering Information

Device	Package	Shipping
SF51G	DO-201AD	
THRU		1250pcs / tape
SF58G	(Pb-Free)	

For information on tape and reel specifications, including part orientation and tape sizes, please refer to our tape and reel packaging specification.

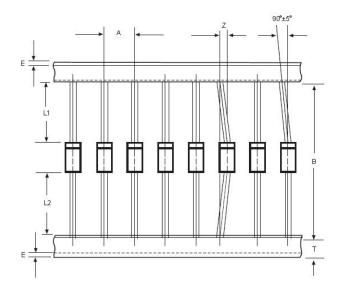
Marking Diagram



Where XXXXX is YYWWL

- = Part Name = SSG
- = SSG = Year
- = Week
- = Lot Number

Carrier Tape Specification DO-201AD



SYMBOL	Millimeters			
	Min.	Max.		
А	9.50	10.50		
В	50.9	53.9		
Z	-	1.20		
Т	5.60	6.40		
E	-	0.80		
IL1-L2I	-	1.0		



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